

## EAST Search History

| Ref # | Hits | Search Query  | DBs                            | Default Operator | Plurals | Time Stamp       |
|-------|------|---|--------------------------------|------------------|---------|------------------|
| L3    | 6510 | (charge ADJ pump) and (logic with (circuit or core))  | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 11:46 |
| L4    | 107  | "714"/\$.ccls. and (charge ADJ pump) and (logic with (circuit or core))   | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 10:45 |
| L5    | 99   | L4 and (@pd<"20040227" or @ad<"20040227" or @prad<"20040227" or @rlad<"20040227")   | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 11:52 |
| L7    | 14   | L5 and scan   | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 10:49 |
| L8    | 3065 | L3 and (reduc\$3 or sav\$3 or low\$5 or minimiz\$5 preserv\$3 or maintain\$3 economiz\$3) near power                      | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 11:50 |
| L9    | 2289 | L8 and (power ADJ supply)   | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 11:51 |
| L11   | 31   | L8 and "714"/\$.ccls.   | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 11:52 |
| L12   | 27   | L11 and (@pd<"20040227" or @ad<"20040227" or @prad<"20040227" or @rlad<"20040227")  | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 12:19 |
| L14   | 15   | (clock ADJ delay) with (("and" ADJ gate) and inverter)  | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 12:59 |
| S2    | 14   | (Ashton Gerry).in. or (Duncan Kevin A).in. or (Keim Terry D).in. or (Saitoh Toshiharu).in. or (Wilder Tad J).in.          | US-PGPUB;<br>USPAT;<br>IBM_TDB | NEAR             | ON      | 2006/10/30 15:17 |
| S8    | 3    | (clock near (tree domain)) and ((phase or control) near delay ) and ((scan near latch) or shift near register near latch) | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 13:13 |
| S9    | 610  | ((clock ADJ delay) (pulse ADJ generator)) with (("and" ADJ gate) and inverter)  | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 12:58 |
| S10   | 15   | (clock ADJ delay) with (("and" ADJ gate) and inverter)  | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR               | ON      | 2006/11/01 10:42 |

## EAST Search History

|     |     |                           |                                |    |    |                  |
|-----|-----|---------------------------|--------------------------------|----|----|------------------|
| S11 | 12  | "714"/\$.ccls. and S9     | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR | ON | 2006/10/31 16:12 |
| S12 | 195 | S9 and (power ADJ supply) | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR | ON | 2006/10/31 16:11 |
| S13 | 3   | "714"/\$.ccls. and S12    | US-PGPUB;<br>USPAT;<br>IBM_TDB | OR | ON | 2006/10/31 16:12 |